

General Description

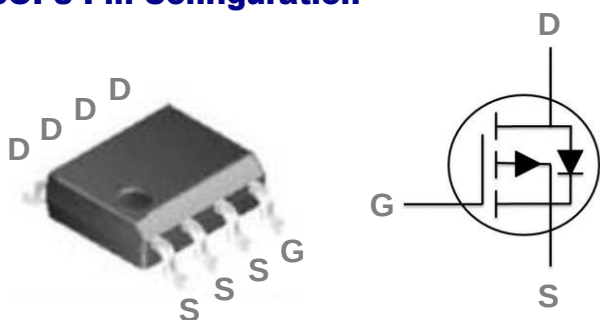
These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BVDSS	RDSON	ID
-60V	47mΩ	-4A

Features

- -60V, -4A, $R_{DS(ON)}$ 47mΩ @ $V_{GS} = -10V$
- Improved dv/dt capability
- Fast switching
- Green Device Available

SOP8 Pin Configuration



Applications

- Networking
- Load Switch
- LED applications

Absolute Maximum Ratings ($T_C=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	-60	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Drain Current – Continuous ($T_A=25^\circ\text{C}$)	-4	A
	Drain Current – Continuous ($T_A=70^\circ\text{C}$)	-3.2	A
I_{DM}	Drain Current – Pulsed ¹	-16	A
EAS	Single Pulse Avalanche Energy ²	58	mJ
IAS	Single Pulse Avalanche Current ²	-34	A
P_D	Power Dissipation ($T_A=25^\circ\text{C}$)	2	W
	Power Dissipation – Derate above 25°C	0.016	W/ $^\circ\text{C}$
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ\text{C}$

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	62.5	$^\circ\text{C/W}$

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)
Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V$, $I_D=-250\mu A$	-60	---	---	V
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=-60V$, $V_{GS}=0V$, $T_J=25^\circ\text{C}$	---	---	-1	μA
		$V_{DS}=-48V$, $V_{GS}=0V$, $T_J=125^\circ\text{C}$	---	---	-10	μA
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20V$, $V_{DS}=0V$	---	---	± 100	nA

On Characteristics

$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS}=-10V$, $I_D=-4A$	---	41	47	$m\Omega$
		$V_{GS}=-4.5V$, $I_D=-3A$	---	53	64	$m\Omega$
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_D=-250\mu A$	-1.2	-1.6	-2.5	V
g_{fs}	Forward Transconductance	$V_{DS}=-10V$, $I_S=-1A$	---	5	---	S

Dynamic and switching Characteristics

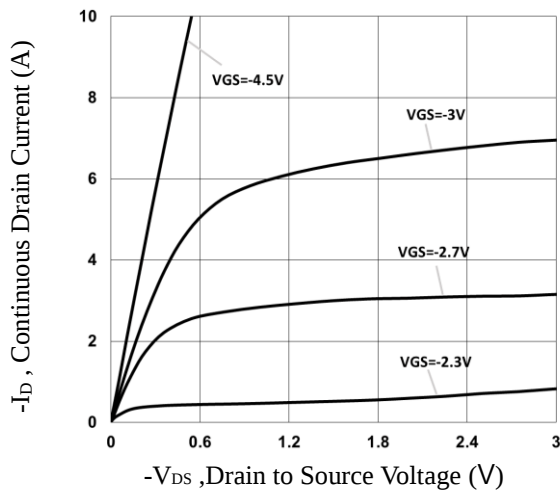
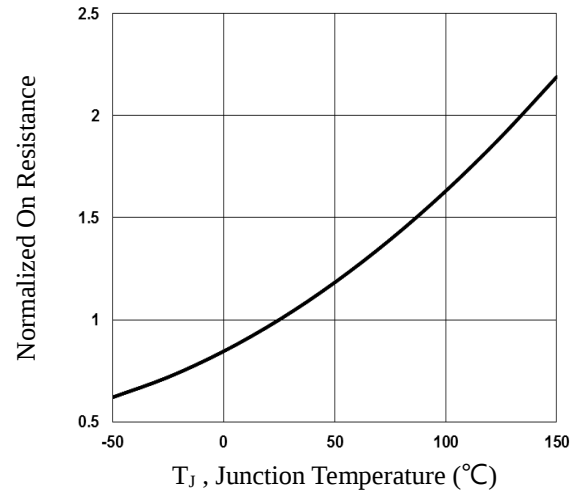
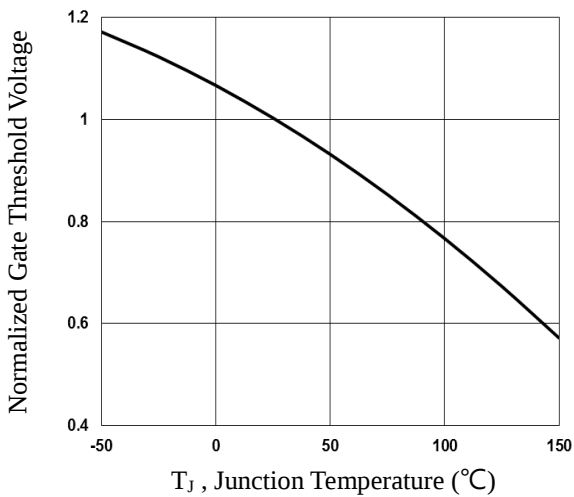
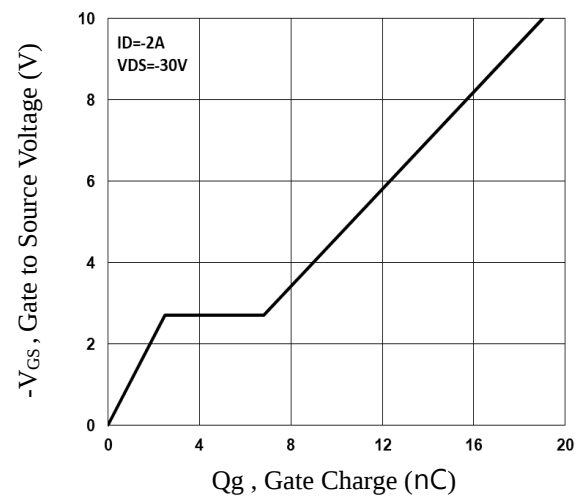
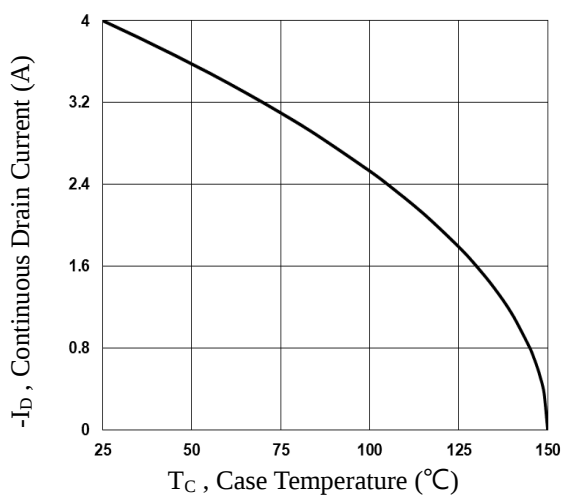
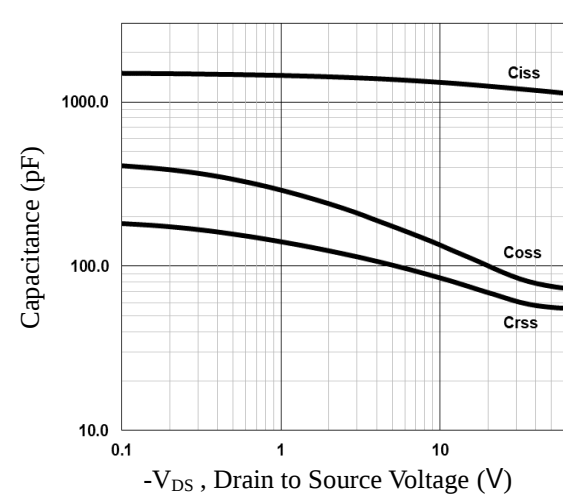
Q_g	Total Gate Charge ^{2, 3}	$V_{DS}=-30V$, $V_{GS}=-10V$, $I_D=-2A$	---	19	30	nC
Q_{gs}	Gate-Source Charge ^{2, 3}		---	2.5	3.8	
Q_{gd}	Gate-Drain Charge ^{2, 3}		---	4.3	6.5	
$T_{d(on)}$	Turn-On Delay Time ^{2, 3}	$V_{DD}=-30V$, $V_{GS}=-10V$, $R_G=6\Omega$ $I_D=-2A$	---	18	27	ns
T_r	Rise Time ^{2, 3}		---	8	12	
$T_{d(off)}$	Turn-Off Delay Time ^{2, 3}		---	100	150	
T_f	Fall Time ^{2, 3}		---	30	45	
C_{iss}	Input Capacitance	$V_{DS}=-30V$, $V_{GS}=0V$, $F=1\text{MHz}$	---	1200	1800	pF
C_{oss}	Output Capacitance		---	85	128	
C_{rss}	Reverse Transfer Capacitance		---	60	90	
R_g	Gate resistance	$V_{GS}=0V$, $V_{DS}=0V$, $F=1\text{MHz}$	---	14	---	Ω

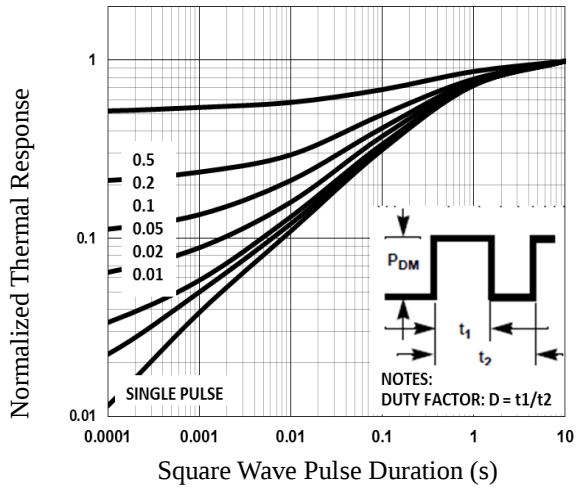
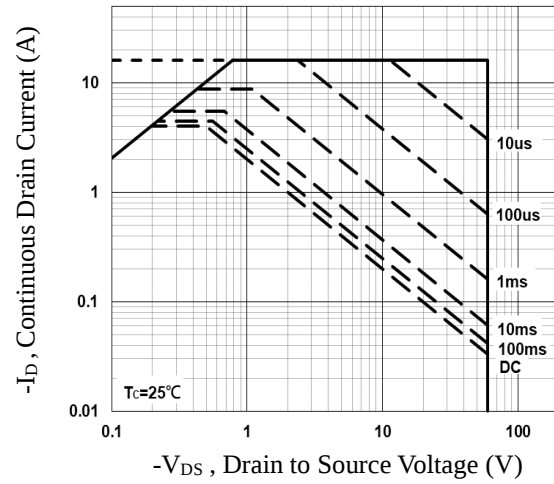
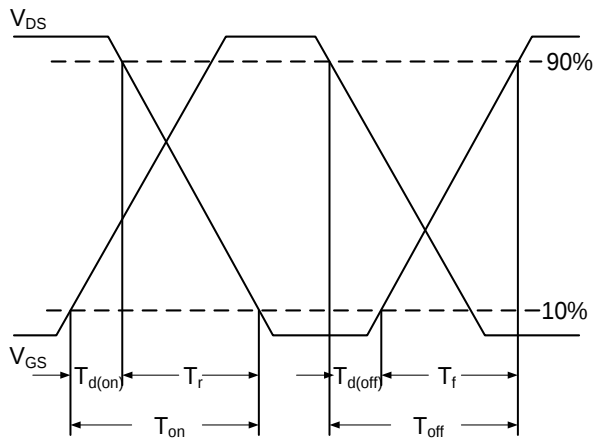
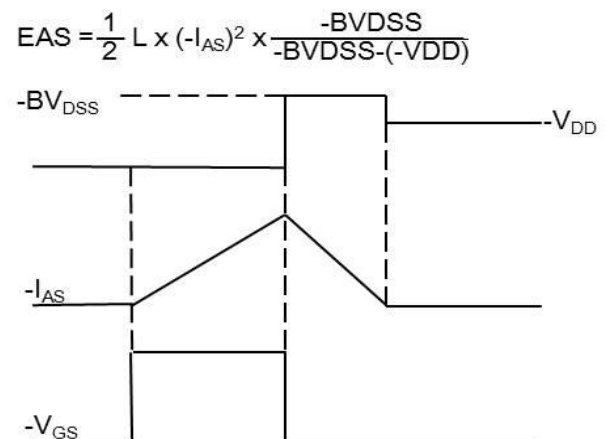
Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current	$V_G=V_D=0V$, Force Current	---	---	-4	A
I_{SM}	Pulsed Source Current		---	---	-8	A
V_{SD}	Diode Forward Voltage	$V_{GS}=0V$, $I_S=-1A$, $T_J=25^\circ\text{C}$	---	---	-1	V
t_{rr}	Reverse Recovery Time	$V_R=-50V$, $I_S=-2A$	---	30	---	ns
Q_{rr}	Reverse Recovery Charge	$di/dt=100A/\mu s$, $T_J=25^\circ\text{C}$	---	20	---	nC

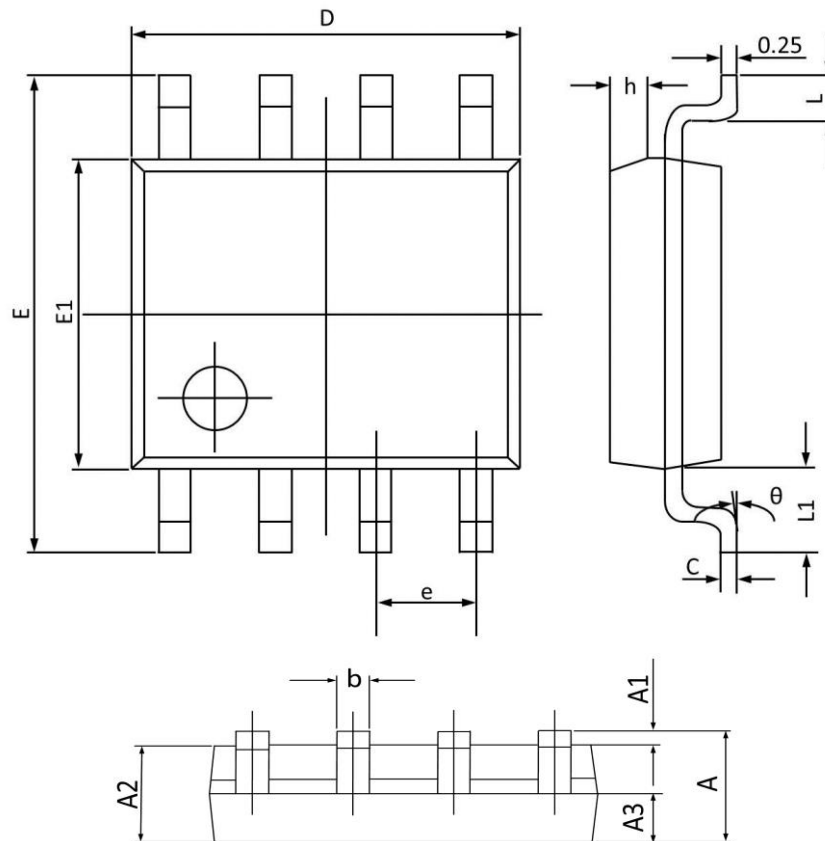
Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. $V_{DD}=-25V$, $V_{GS}=-10V$, $L=0.1mH$, $I_{AS}=-34A$, Starting $T_J=25^\circ\text{C}$
3. The data tested by pulsed , pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.
4. Essentially independent of operating temperature.


Fig.1 Typical Output Characteristics

Fig.2 Normalized $R_{DS(on)}$ vs. T_J

Fig.3 Normalized V_{th} vs. T_J

Fig.4 Gate Charge Waveform

Fig.5 Continuous Drain Current vs. T_J

Fig.6 Capacitance Characteristics


Fig.7 Normalized Transient Impedance

Fig.8 Maximum Safe Operation Area

Fig.9 Switching Time Waveform

Fig.10 EAS Waveform

SOP8 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.350	1.800	0.053	0.069
A1	0.050	0.250	0.002	0.010
A2	1.250	1.650	0.049	0.065
A3	0.500	0.700	0.020	0.028
b	0.300	0.510	0.012	0.020
c	0.150	0.260	0.006	0.010
D	4.700	5.100	0.185	0.201
E	5.800	6.200	0.228	0.244
E1	3.700	4.100	0.146	0.161
e	1.270(BSC)		0.050(BSC)	
h	0.250	0.500	0.010	0.020
L	0.400	1.000	0.016	0.039
L1	1.050(BSC)		0.041(BSC)	
θ	0°	8°	0°	8°